

Image AF/1765



501.40201X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. TADOKORO, et al.
Serial No: 09/893,577
Filed: June 29, 2001
Title: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Group: 1765
Examiner: Kin Chan CHEN

AMENDMENT AFTER FINAL REJECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 30, 2004

Sir:

In response to the Office Action mailed June 10, 2003, and subsequent to the filing of a Notice of Appeal on December 10, 2003, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.